

Docket No.: 49657-875



PATENT

SC  
#11C  
11/6/03

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of	:	Customer Number: 20277
Tetsushi TANIZAKI, et al.	:	Confirmation Number: 8725
Serial No.: 09/712,246	:	Group Art Unit: 2133
Filed: November 15, 2000	:	Examiner: M.M. Chaudry
For: SEMICONDUCTOR MEMORY DEVICE WITH BUILT-IN SELF TEST CIRCUIT OPERATING AT HIGH RATE	:	

**AMENDMENT**

Mail Stop RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RECEIVED**  
OCT 20 2003  
Technology Center 2100

Sir:

The following amendment and remarks are submitted in response to the Office

Action dated July 10, 2003.

Amendments to the claims begin on page 2 of this paper.

Remarks begin on page 10 of this paper.